



PJM80P20DN

P-Channel Enhancement Mode Power MOSFET

Product Summary

- $V_{DS} = -20V, I_D = -85A$
- $R_{DS(on)} < 4.5m\Omega @ V_{GS} = -4.5V$
- $R_{DS(on)} < 5.9m\Omega @ V_{GS} = -2.5V$

Features

- Advanced Trench Technology
- 100% Avalanche Tested
- 100% DVDS
- RoHS Compliant
- Halogen and Antimony Free
- Moisture Sensitivity Level 3
- Molding Compound Meets UL 94 V-0 Flammability Rating

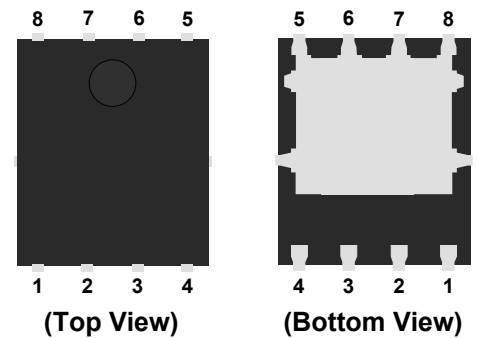
Application

- Load Switch
- PWM Application
- Power Management

Marking Code

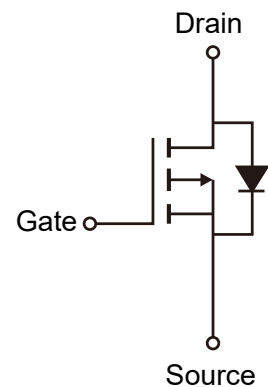


PDFN5x6-8L



Pin	Description
1,2,3	Source
4	Gate
5,6,7,8	Drain

Schematic Diagram



Absolute Maximum Ratings

Ratings at 25°C case temperature unless otherwise specified.

Parameter		Symbol	Value	Unit
Drain-Source Voltage		$-V_{DS}$	20	V
Gate-Source Voltage		V_{GS}	± 12	V
Drain Current-Continuous	$T_C = 25^\circ C$	$-I_D$	85	A
	$T_C = 100^\circ C$		51	
Drain Current-Pulsed ^{Note1}		$-I_{DM}$	340	A
Single Pulse Avalanche Energy ^{Note2}		E_{AS}	72	mJ
Maximum Power Dissipation		P_D	58	W
Junction and Storage Temperature Range		T_J, T_{STG}	-55 to +150	°C

Thermal Characteristics

Thermal Resistance, Junction-to-Ambient ^{Note3}	$R_{\theta JA}$	42	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	2.2	°C/W



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Electrical Characteristics

(T_J=25°C unless otherwise specified)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	-V _{(BR)DSS}	V _{GS} =0V,I _D =-250μA	20	--	--	V
Zero Gate Voltage Drain Current	-I _{DSS}	V _{DS} =-20V,V _{GS} =0V	--	--	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±12V,V _{DS} =0V	--	--	±100	nA
Gate Threshold Voltage ^{Note4}	-V _{GS(th)}	V _{DS} =V _{GS} ,I _D =-250μA	0.4	0.65	1	V
Drain-Source On-Resistance ^{Note4}	R _{DS(on)}	V _{GS} =-4.5V,I _D =-20A	--	3.4	4.5	mΩ
		V _{GS} =-2.5V,I _D =-15A	--	4.5	5.9	mΩ
Forward Transconductance ^{Note4}	g _{FS}	V _{DS} =-5V,I _D =-2A	--	10.5	--	S
Dynamic Characteristics						
Input Capacitance	C _{iss}	V _{DS} =-10V,V _{GS} =0V,f=1MHz	--	5520	--	pF
Output Capacitance	C _{oss}		--	753	--	pF
Reverse Transfer Capacitance	C _{rss}		--	570	--	pF
Gate resistance	R _g	V _{DS} =0V,V _{GS} =0V,f=1MHz	--	12	--	Ω
Total Gate Charge	Q _g	V _{DS} =-10V,I _D =-10A, V _{GS} =-4.5V	--	56	--	nC
Gate-Source Charge	Q _{gs}		--	10	--	nC
Gate-Drain Charge	Q _{gd}		--	10.5	--	nC
Switching Characteristics						
Turn-on Delay Time	t _{d(on)}	V _{DD} =-10V,I _D =-10A, V _{GS} =-4.5V,R _{GEN} =1Ω	--	27	--	nS
Turn-on Rise Time	t _r		--	55	--	nS
Turn-off Delay Time	t _{d(off)}		--	70	--	nS
Turn-off Fall Time	t _f		--	90	--	nS
Source-Drain Diode Characteristics						
Diode Forward Voltage ^{Note4}	-V _{SD}	V _{GS} =0V,I _S =-20A	--	--	1.2	V
Diode Forward Current	-I _S		--	--	85	A
Reverse Recovery Time	trr	I _F =-10A, di/dt=100A/μs	--	39	--	nS
Reverse Recovery Charge	Qrr		--	21	--	nC

Note:

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. E_{AS} condition: T_J=25°C, V_{DD}=-15V, V_G=-10V, R_G=25Ω, L=0.5mH, I_{AS}=-17A
3. Surface mounted on 1inch² FR-4 board with 2OZ copper
4. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%

Test Circuit

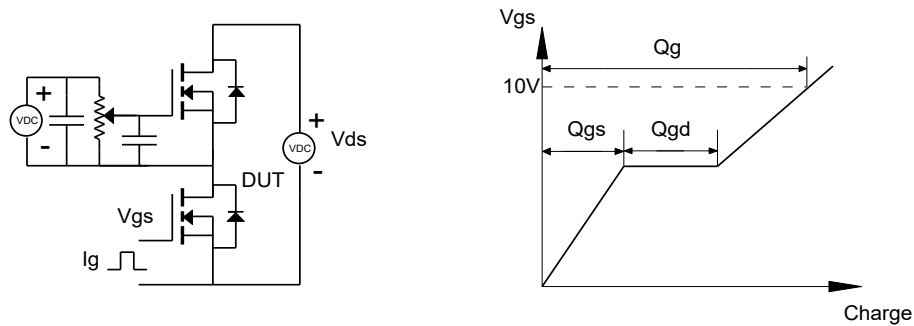


Figure 1: Gate Charge Test Circuit & Waveform

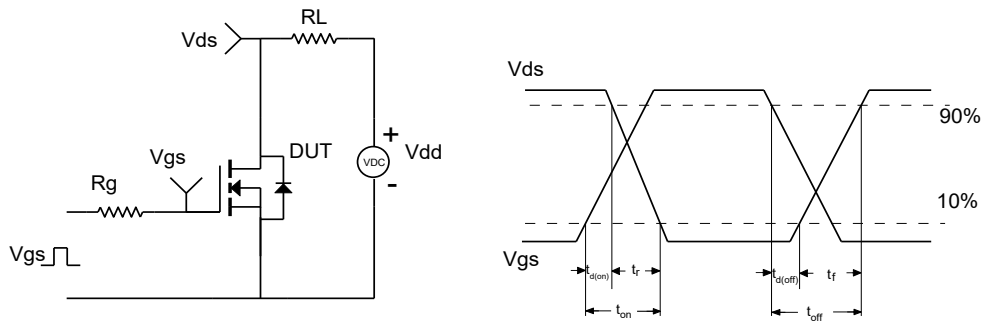


Figure 2: Resistive Switching Test Circuit & Waveform

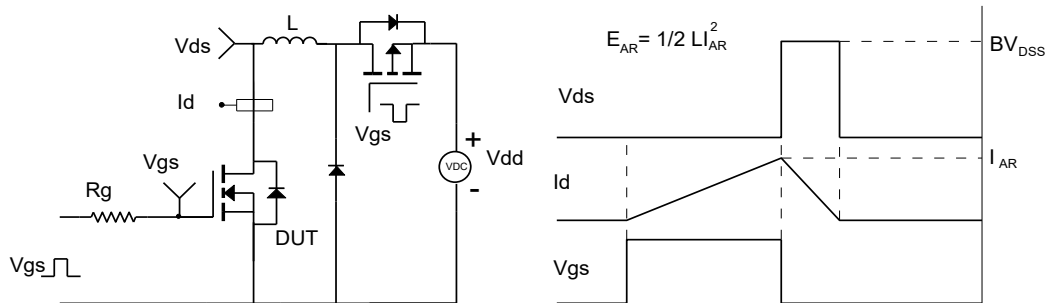


Figure 3: Unclamped Inductive Switching Test Circuit & Waveform

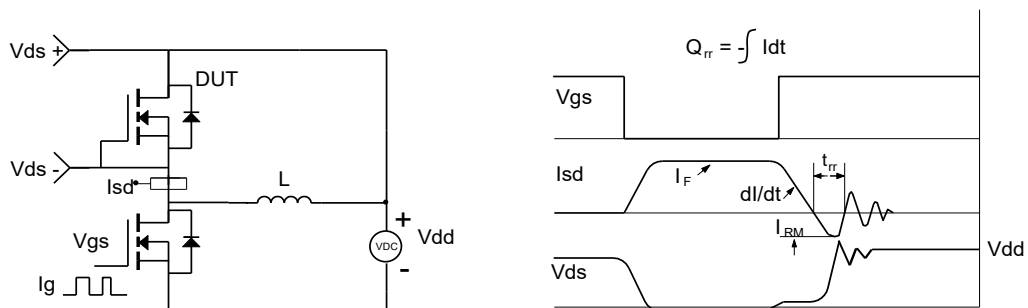


Figure 4: Diode Recovery Test Circuit & Waveform

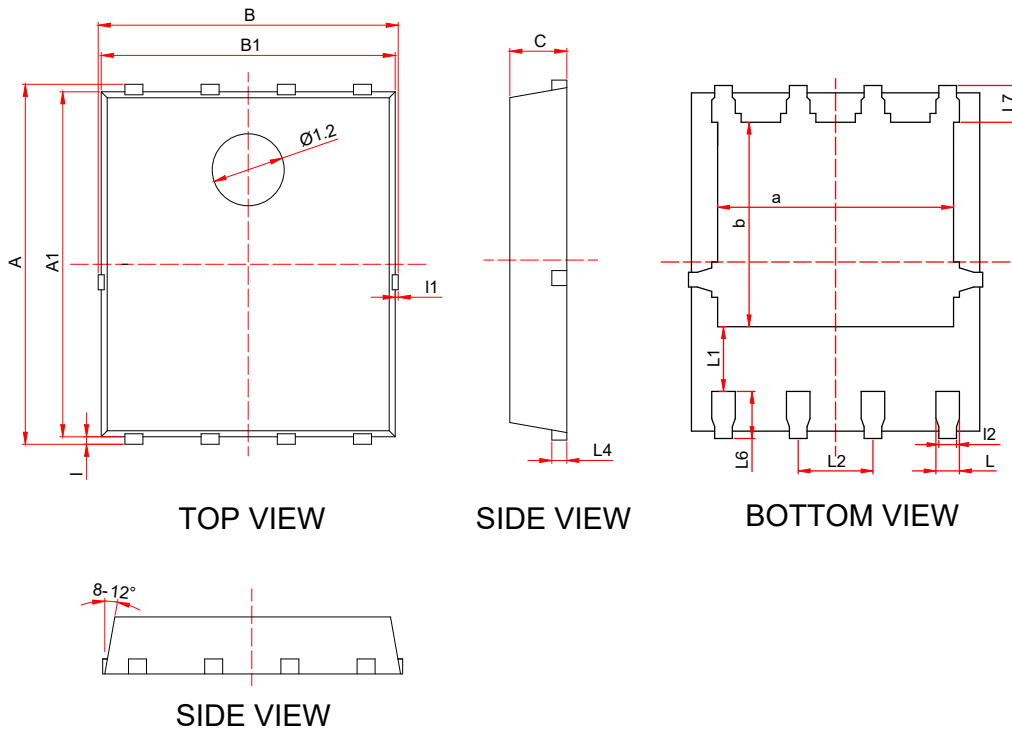


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Package Outline

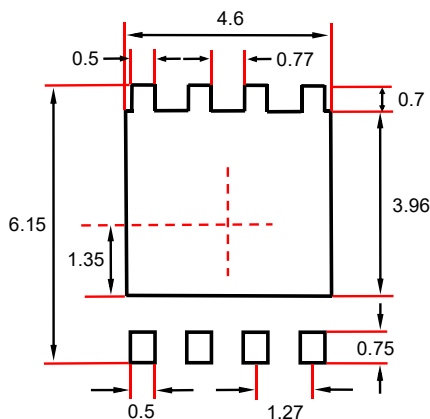
PDFN5x6-8L



Symbol	Dimensions in Millimeters			Dimensions in Inches		
	Min.	Nom.	Max.	Min.	Nom.	Max.
A	5.850	6.000	6.150	0.230	0.236	0.242
A1	5.700	5.800	5.900	0.224	0.228	0.232
B	4.900	5.100	5.300	0.193	0.201	0.209
B1	4.800	5.000	5.200	0.189	0.197	0.205
I	0.060	0.130	0.200	0.002	0.005	0.008
I1	0.000	-	0.100	0.000	-	0.004
I2	0.250	0.300	0.350	0.010	0.012	0.014
C	0.950	1.000	1.050	0.037	0.039	0.041
L4	0.200	0.250	0.300	0.008	0.010	0.012
L7	0.500	0.600	0.700	0.020	0.024	0.028
a	4.100	4.200	4.300	0.161	0.165	0.169
b	3.400	3.500	3.600	0.134	0.138	0.142
L1	1.200	1.300	1.400	0.047	0.051	0.055
L6	0.550	0.650	0.750	0.022	0.026	0.030
L2	1.170	1.270	1.370	0.046	0.050	0.054
L	0.300	0.400	0.500	0.012	0.016	0.020

Suggested Pad Layout

PDFN5x6-8L



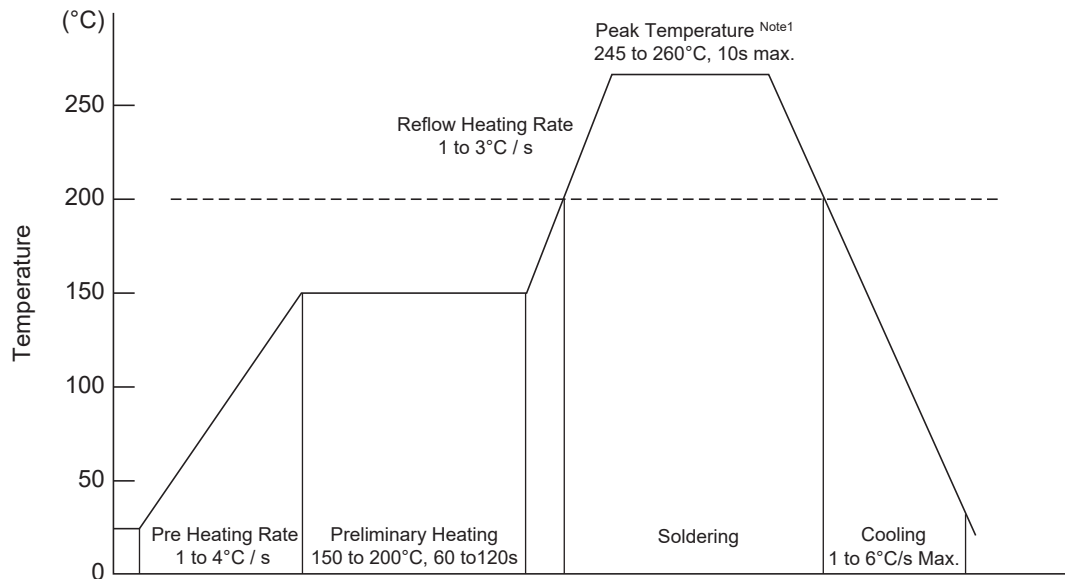
Note:

1. Package Body Sizes Exclude Mold Flash and Gate Burrs
2. Controlling Dimension: In millimeters
3. General Tolerance: $\pm 0.1\text{mm}$
4. The Pad Layout is for reference purposes only



Conditions of Soldering and Storage

◆ Recommended condition of Reflow Soldering(Pb-Free Solder)



Recommended peak temperature is over 245°C. If peak temperature is below 245°C, you may adjust the following parameters:

- Time length of peak temperature (longer)
- Time length of soldering (longer)
- Thickness of solder paste (thicker)

Note1:

Maximum thermal excursion allowed during the reflow assembly is as follow:

- Temperature: 255°C~260°C
- Duration at peak soak : 30 seconds
- Number of reflow: 3
- **Conditions of hand soldering** ^{Note2}

- Temperature: 320±10°C
- Time: 3s max.
- Times: one time

Note2: Not recommended for mass production, an engineering project, or re-work, it is allowed. It should be used with caution

● **Storage conditions**

● **Temperature**

5 to 30°C

● **Humidity**

≤ 60% RH

● **Floor Life**

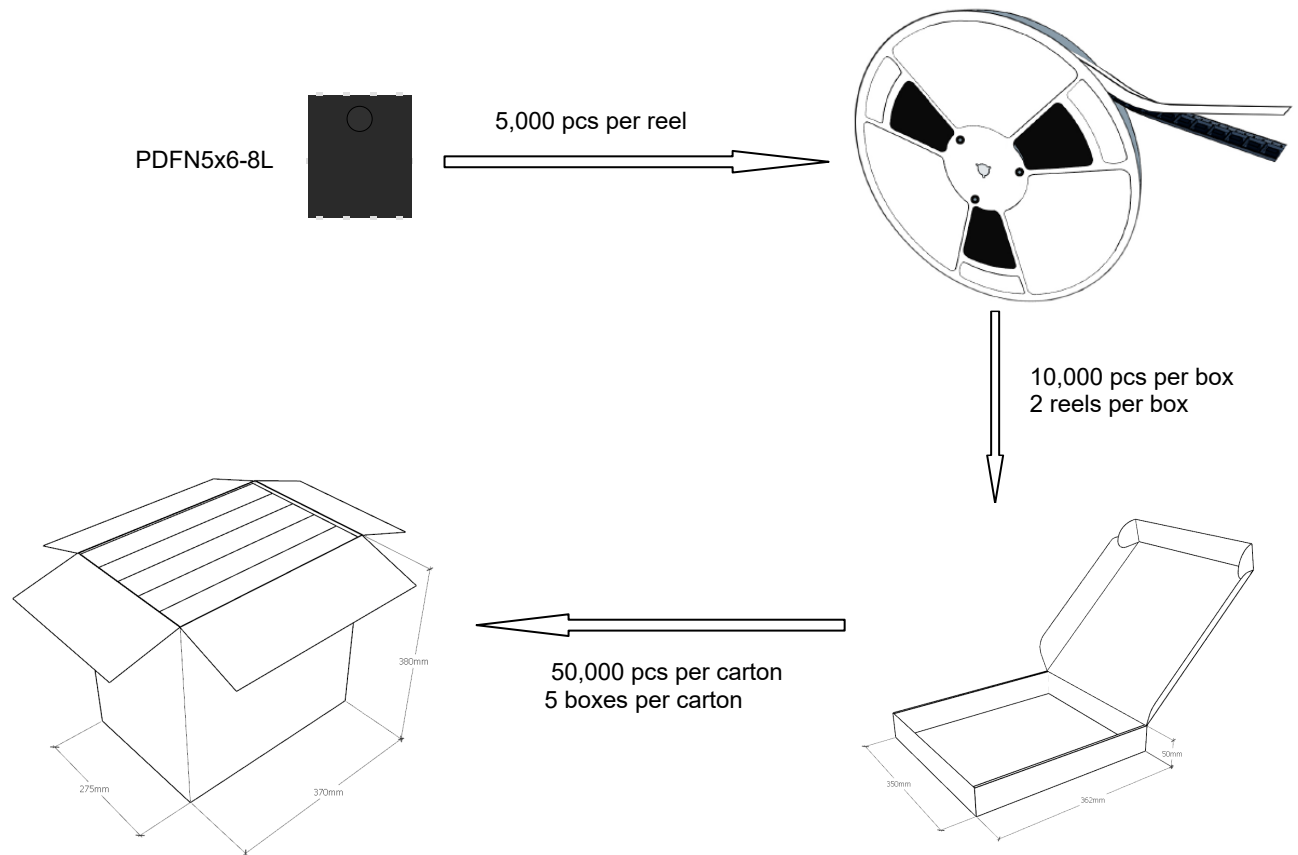
168 Hours

● **Recommended period**

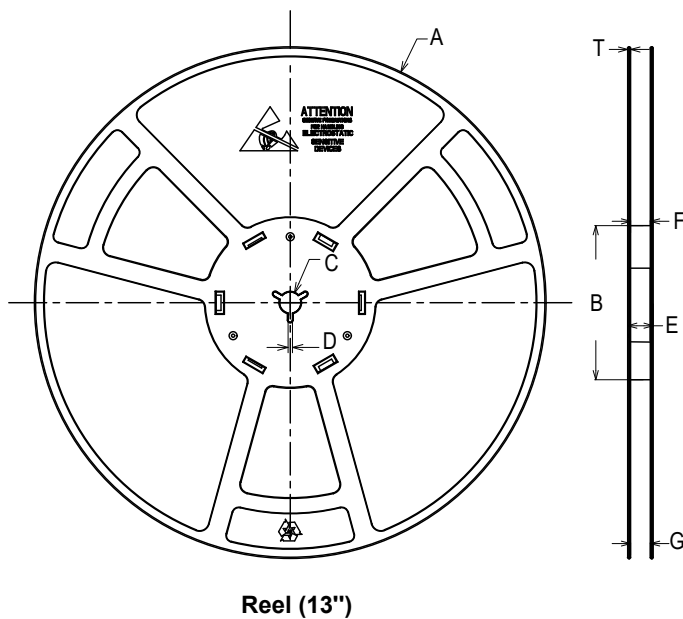
Five year after manufacturing(Depends on storage conditions)

Package Specifications

- The method of packaging



◆ reel data



Symbol	Value(unit:mm)
A	$\Phi 330 \pm 2$
B	$\Phi 99.5 \pm 0.5$
C	13.6 ± 0.3
D	2.8 ± 0.2
E	16.2 ± 0.5
F	$13^{+0.8}_{-0.3}$
G	13 ± 3
T	1.6 ± 0.2



Technical drawing of a rectangular plate with four square holes and four circular holes. The drawing includes dimensions: 4.00±0.10, 8.00±0.10, 2.00±0.05, 1.75±0.10, 5.50±0.05, 12.00±0.20, and a hole diameter of Ø1.55±0.05. Below the plate are four square images showing the internal structure of the holes.