



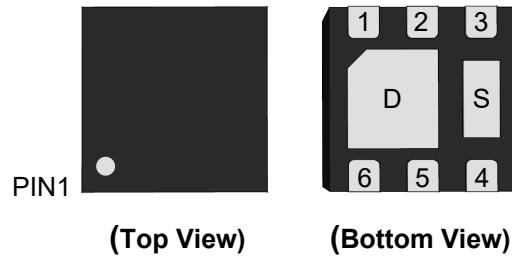
# PJM18P15DF

## P-Channel Enhancement Mode Power MOSFET

### Product Summary

- $V_{DS} = -15V, I_D = -18A$
- $R_{DS(on)} < 18m\Omega @ V_{GS} = -4.5V$
- $R_{DS(on)} < 24m\Omega @ V_{GS} = -2.5V$

DFN2x2-6L



### Features

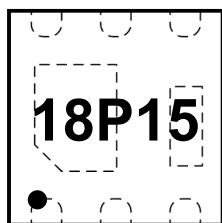
- Advanced Trench Technology
- RoHS and Reach Compliant
- Halogen and Antimony Free
- Moisture Sensitivity Level 1

### Application

- Load Switch
- PWM Applications
- Power Management

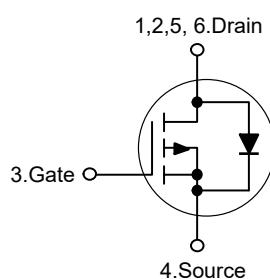
Pin	Description
1,2,5,6	Drain
3	Gate
4	Source

### Marking Code



Top View

### Schematic Diagram



### Absolute Maximum Ratings

Ratings at 25°C Case temperature unless otherwise specified.

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$-V_{DS}$	15	V
Gate-Source Voltage	$-V_{GS}$	$\pm 12$	V
Drain Current-Continuous	$-I_D$	18	A
Drain Current-Pulsed <sup>Note1</sup>	$-I_{DM}$	64	A
Maximum Power Dissipation	$P_D$	8	W
Single Pulsed Avalanche Energy <sup>Note2</sup>	$E_{AS}$	22.5	mJ
Junction Temperature	$T_J$	150	°C
Storage Temperature Range	$T_{STG}$	-55 to +150	°C

### Thermal Characteristics

Thermal Resistance, Junction-to-Case	$R_{eJC}$	15.6	°C/W
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### Electrical Characteristics

(Ta=25°C unless otherwise specified)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	-V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =-250μA	15	--	--	V
Zero Gate Voltage Drain Current	-I <sub>DSS</sub>	V <sub>DS</sub> =-15V, V <sub>GS</sub> =0V	--	--	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±12V, V <sub>DS</sub> =0V	--	--	±100	nA
Gate Threshold Voltage <sup>Note3</sup>	-V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA	0.4	0.6	1	V
Drain-Source On-Resistance <sup>Note3</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-8A	--	12	18	mΩ
		V <sub>GS</sub> =-2.5V, I <sub>D</sub> =-5A	--	16.5	24	mΩ
Forward Transconductance <sup>Note3</sup>	g <sub>FS</sub>	V <sub>DS</sub> =-5V, I <sub>D</sub> =-1A	--	7.9	--	S
<b>Dynamic Characteristics</b>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =-6V, V <sub>GS</sub> =0V, f=1MHz	--	1332	--	pF
Output Capacitance	C <sub>oss</sub>		--	278	--	pF
Reverse Transfer Capacitance	C <sub>rss</sub>		--	235	--	pF
Gate Resistance	R <sub>g</sub>	V <sub>DS</sub> =-0V, V <sub>GS</sub> =0V, f=1MHz	--	14	--	Ω
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =-6V, I <sub>D</sub> =-8A, V <sub>GS</sub> =-4.5V	--	35	--	nC
Gate-Source Charge	Q <sub>gs</sub>		--	5	--	nC
Gate-Drain Charge	Q <sub>gd</sub>		--	10	--	nC
<b>Switching Characteristics</b>						
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =-6V, I <sub>D</sub> =-8A, V <sub>GS</sub> =-4.5V, R <sub>GEN</sub> =2.5Ω	--	11	--	nS
Turn-on Rise Time	t <sub>r</sub>		--	35	--	nS
Turn-off Delay Time	t <sub>d(off)</sub>		--	30	--	nS
Turn-off Fall Time	t <sub>f</sub>		--	10	--	nS
<b>Source-Drain Diode Characteristics</b>						
Diode Forward Voltage <sup>Note3</sup>	-V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>s</sub> =-18A	--	--	1.2	V
Diode Forward Current	-I <sub>s</sub>		--	--	18	A

Note: 1. Repetitive Rating: Pulse width limited by maximum junction temperature.

2. The test condition is V<sub>DD</sub>=10V, V<sub>G</sub>=10V, L=0.5mH, I<sub>AS</sub>=9.5A, TJ=25°C.

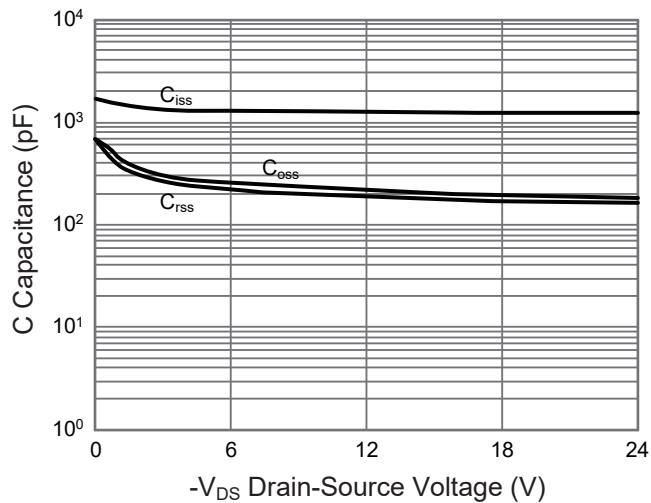
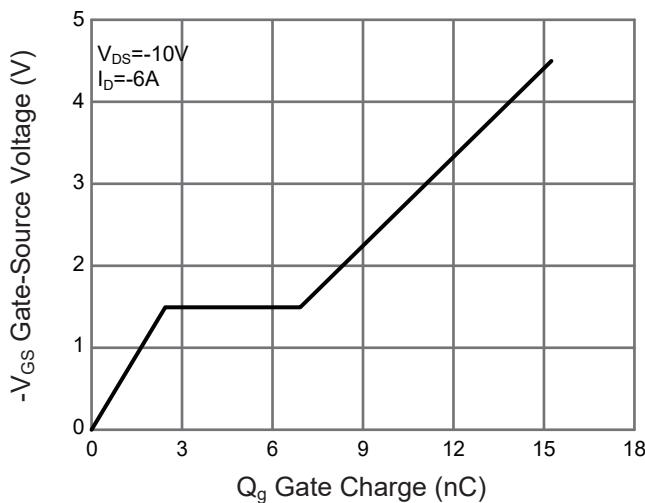
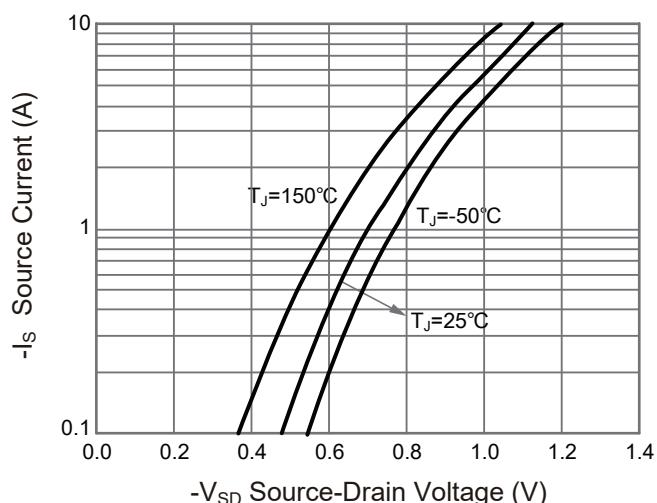
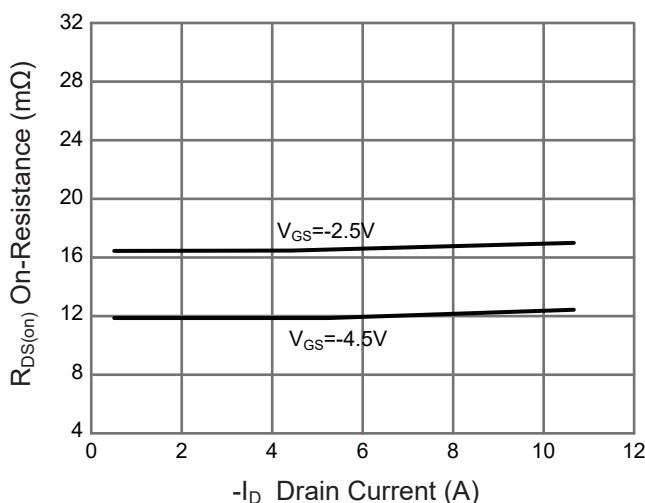
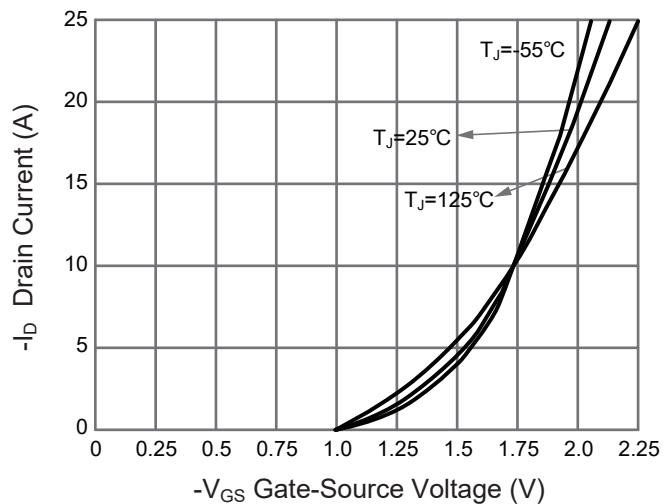
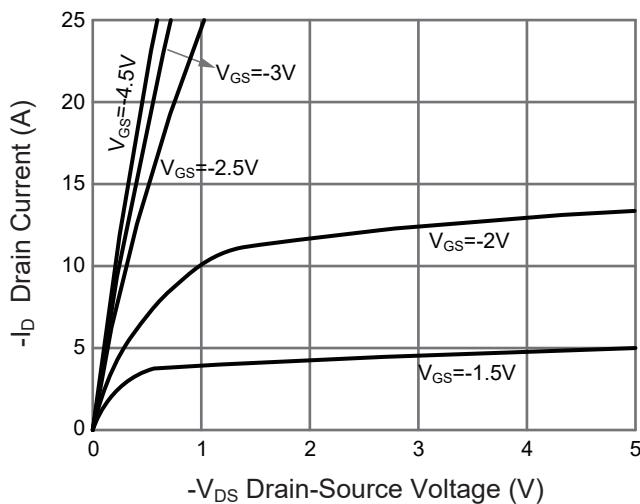
3. Pulse Test: Pulse width≤300μs, duty cycles≤2%.

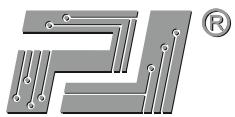


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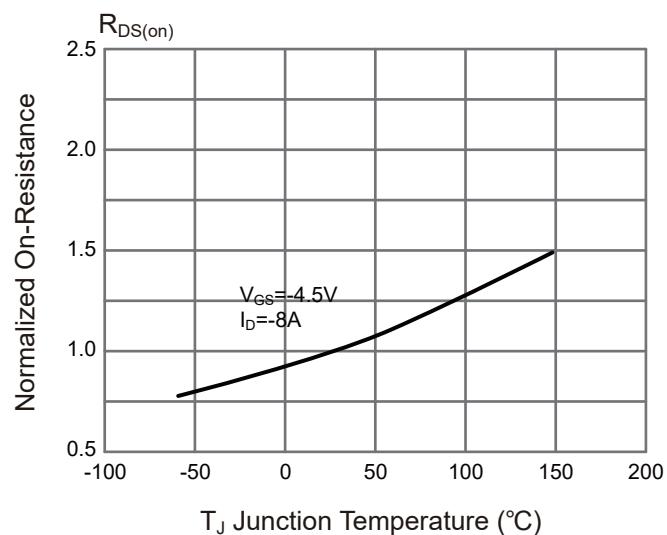
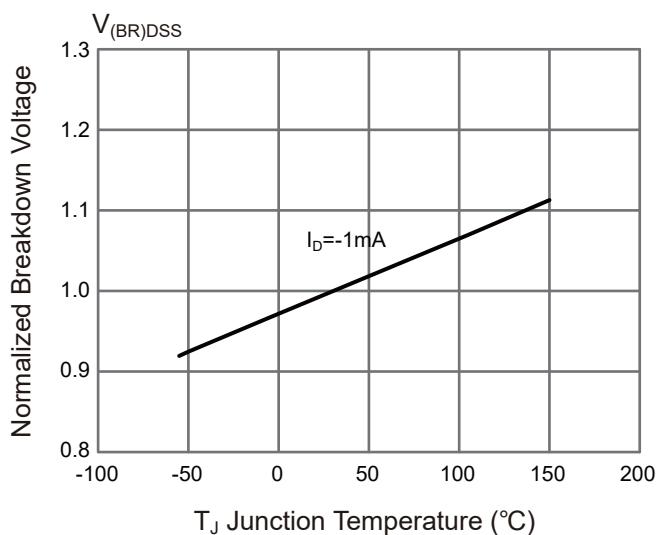
### Typical Characteristic Curves





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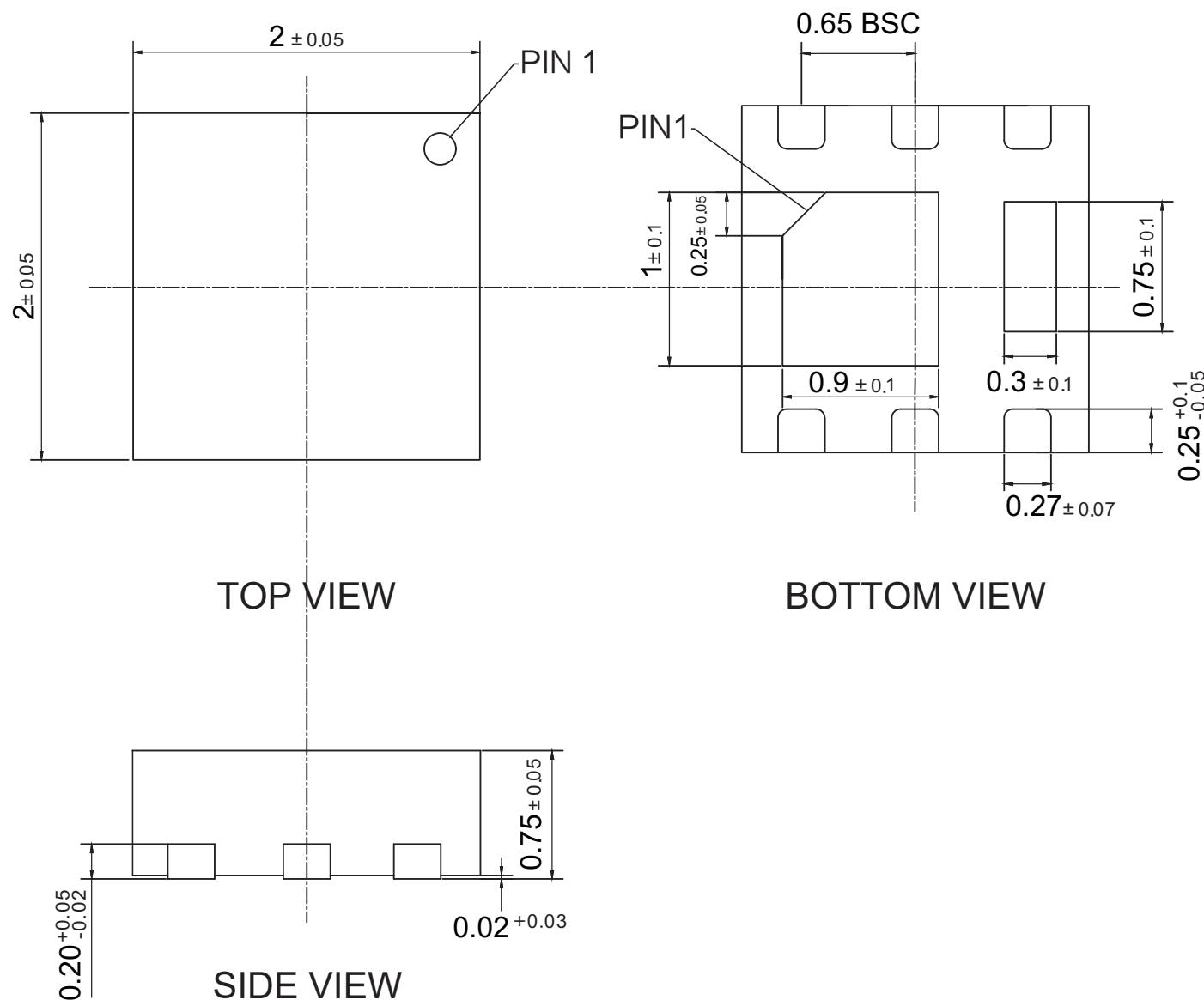
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## P-Channel Enhancement Mode Power MOSFET

### Package Outline

DFN2x2-6L-0001

Dimensions in mm



### Ordering Information

Device	Package	Shipping
PJM18P15DF	DFN2x2A-6L	3,000PCS/Reel&7inches