



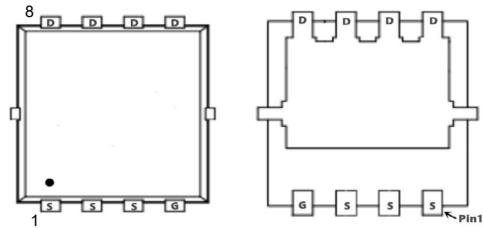
PJM200N40DN

N-Channel Enhancement Mode Power MOSFET

Features

- Excellent $R_{DS(ON)}$ and Low Gate Charge
- $V_{DS}=40V, I_D= 200A$
- $R_{DS(on)} < 1.0m\Omega @ V_{GS} = 10V$

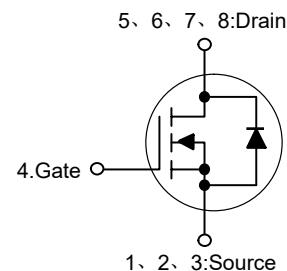
PDFN5x6-8L



Schematic Diagram

Applications

- BMS
- BLDC
- UPS



Absolute Maximum Ratings

Ratings at 25°C junction temperature unless otherwise specified.

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	40	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous at $V_{GS}=10V$ Note ¹ ($T_c=25^\circ C$)	I_D	200	A
Drain Current-Pulsed Note ²	I_{DM}	800	A
Single Pulse Avalanche Energy Note ³	E_{AS}	420	mJ
Avalanche Current	I_{AS}	70	A
Maximum Power Dissipation Note ⁴ ($T_c=25^\circ C$)	P_D	68	W
Junction Temperature	T_J	150	°C
Storage Temperature Range	T_{STG}	-55 to +150	°C

Thermal Characteristics

Thermal Resistance, Junction-to-Ambient Note ¹	$R_{\theta JA}$	25	°C/W
Thermal Resistance, Junction-to-Case Note ¹	$R_{\theta JC}$	1.4	°C/W



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Electrical Characteristics

($T_J=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	40	48	--	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}}=40\text{V}, V_{\text{GS}}=0\text{V}$	--	--	1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	--	--	± 100	nA
Gate Threshold Voltage ^{Note2}	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	1.2	1.5	2.5	V
Drain-Source On-Resistance ^{Note2}	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=30\text{A}$	--	0.75	1.0	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=20\text{A}$	--	1.1	1.5	$\text{m}\Omega$
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{\text{DS}}=20\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	--	7400	--	pF
Output Capacitance	C_{oss}		--	1930	--	pF
Reverse Transfer Capacitance	C_{rss}		--	110	--	pF
Switching Characteristics						
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}}=20\text{V}, I_{\text{D}}=85\text{A}, V_{\text{GS}}=10\text{V}, R_{\text{G}}=1.6\Omega$	--	14.1	--	nS
Turn-on Rise Time	t_{r}		--	7.9	--	nS
Turn-off Delay Time	$t_{\text{d}(\text{off})}$		--	56.5	--	nS
Turn-off Fall Time	t_{f}		--	9.6	--	nS
Total Gate Charge	Q_{g}	$V_{\text{DS}}=20\text{V}, I_{\text{D}}=85\text{A}, V_{\text{GS}}=10\text{V}$	--	125	--	nC
Gate-Source Charge	Q_{gs}		--	18	--	nC
Gate-Drain Charge	Q_{gd}		--	13	--	nC
Source-Drain Diode Characteristics						
Diode Forward Voltage ^{Note2}	V_{SD}	$V_{\text{GS}}=0\text{V}, I_{\text{s}}=30\text{A}$	--	--	1.2	V
Diode Forward Current ^{Note1,5}	I_{s}		--	--	200	A

Note :

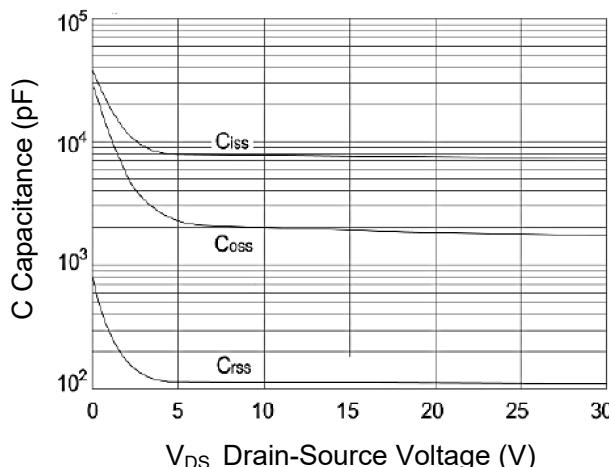
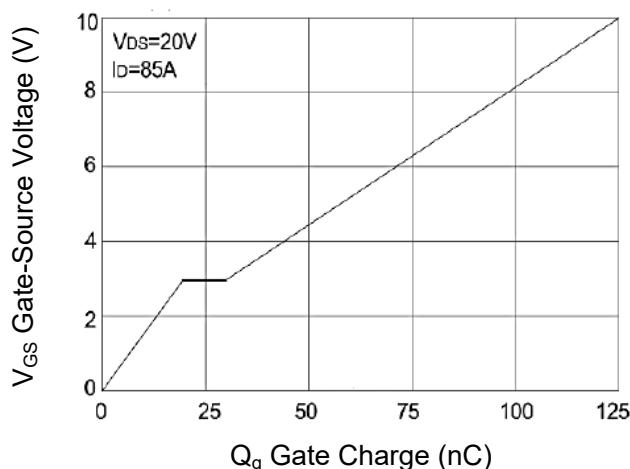
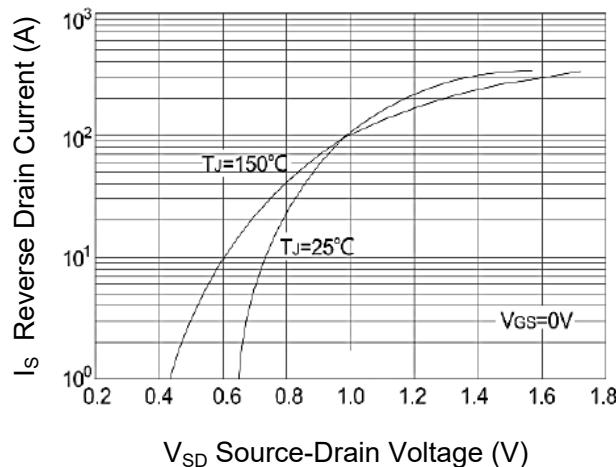
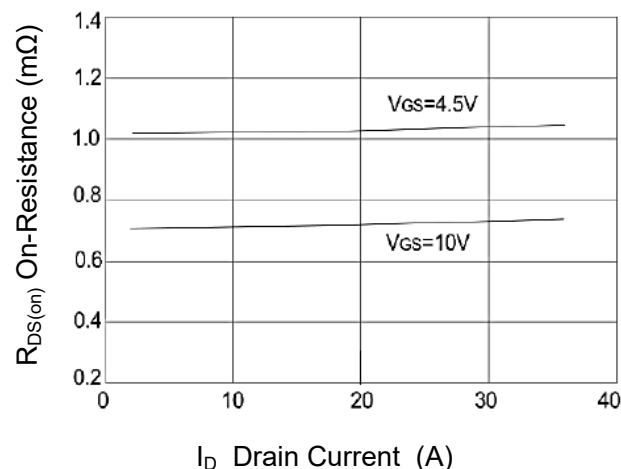
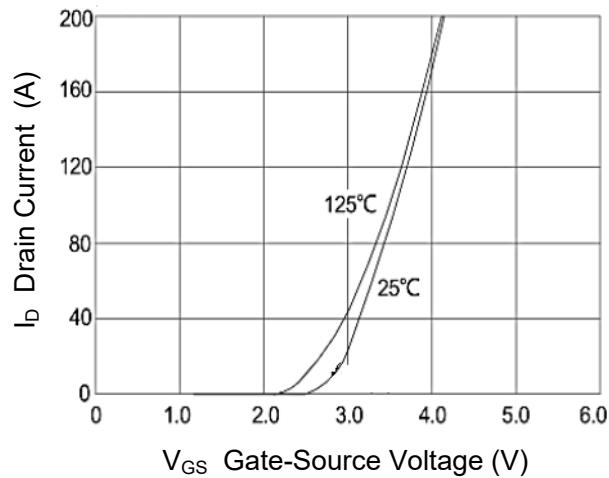
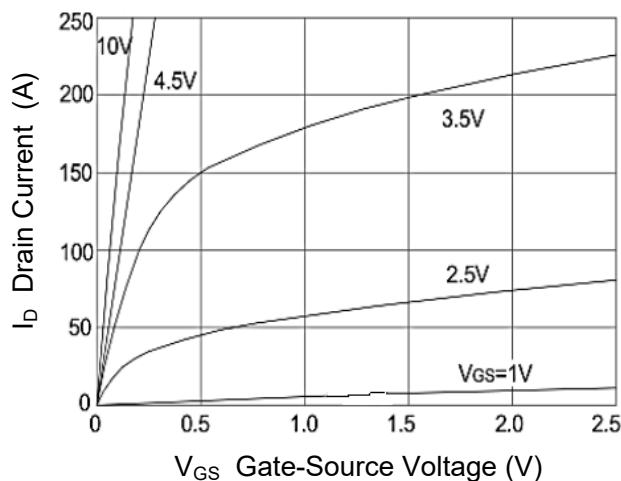
- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
- 3.The E_{AS} data shows Max. rating . The test condition is $V_{\text{DD}}=32\text{V}, V_{\text{GS}}=10\text{V}, L=0.1\text{mH}, I_{\text{AS}}=70\text{A}$
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I_{D} and I_{DM} , in real applications , should be limited by total power dissipation.



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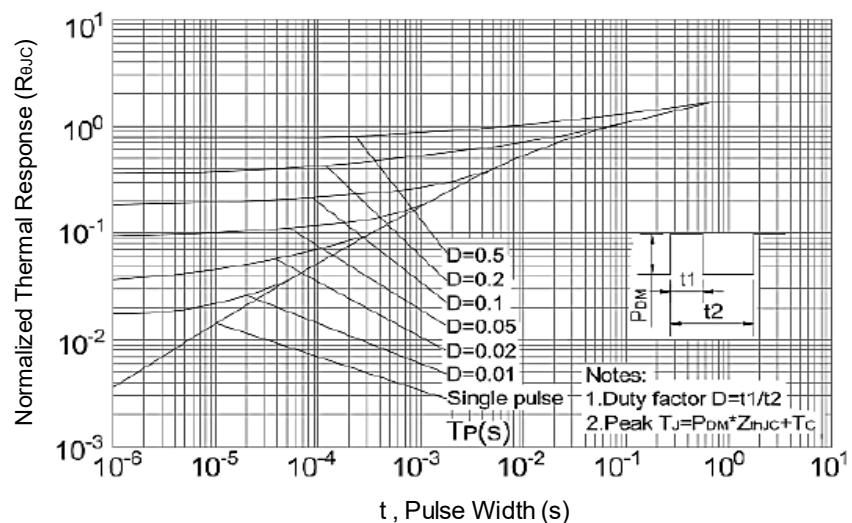
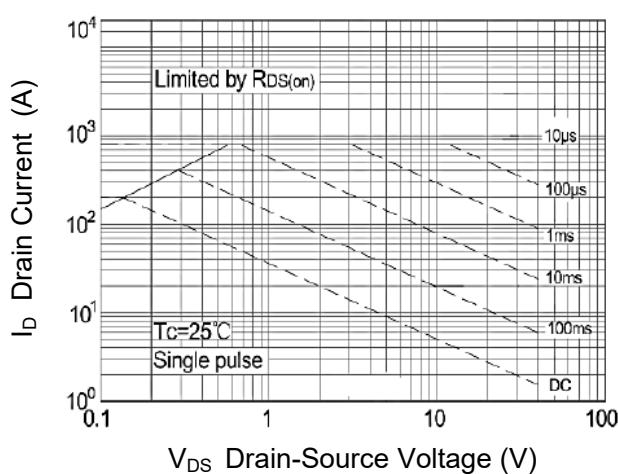
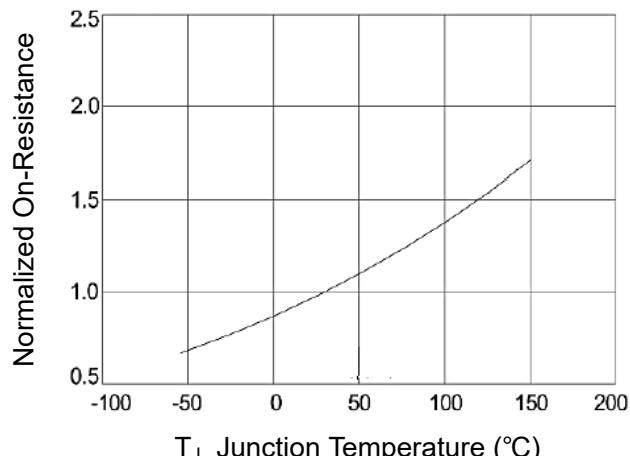
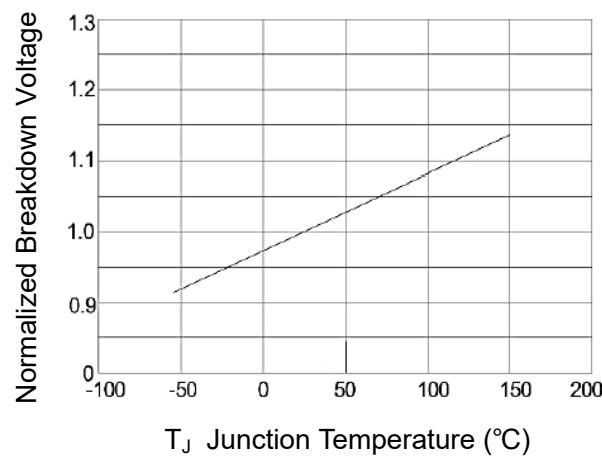
Typical Characteristic Curves





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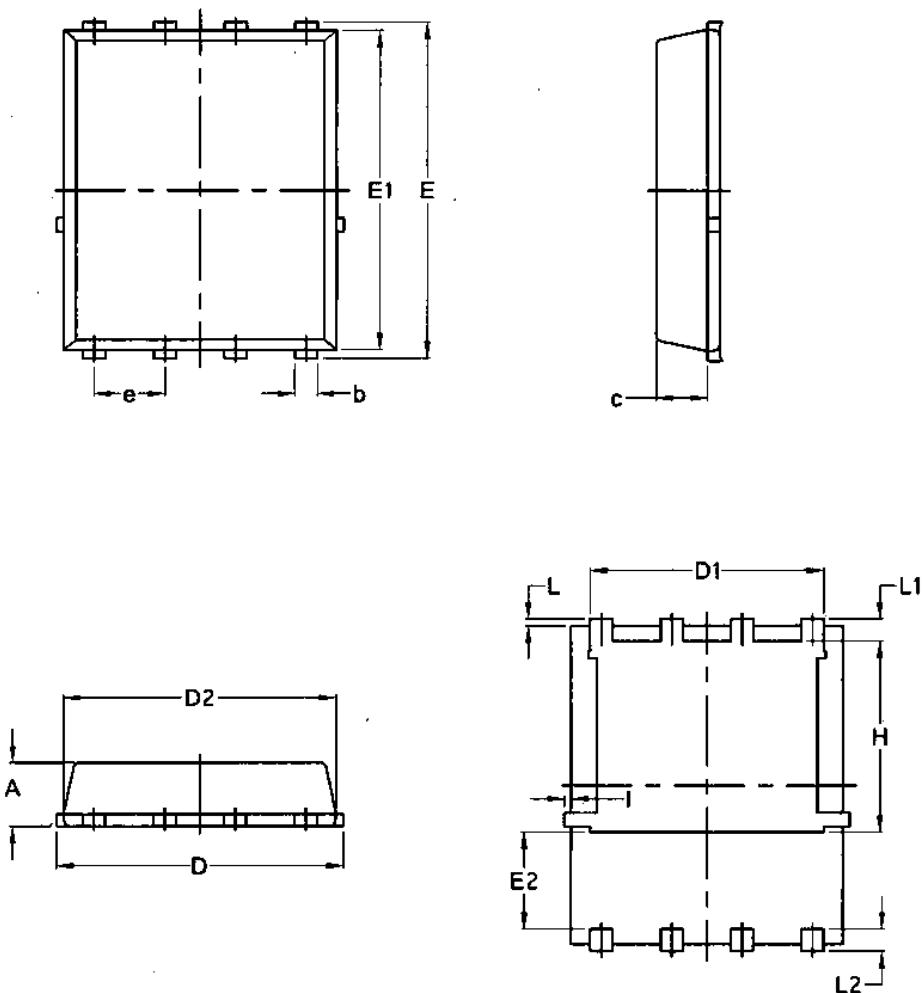
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Package Outline

PDFN5x6-8L

Dimensions in mm



Symbol	Common			
	mm		Inch	
	Min	Max	Min	Max
A	1.03	1.17	0.0406	0.0461
b	0.34	0.48	0.0134	0.0189
c	0.824	0.970	0.0324	0.082
D	4.80	5.40	0.1890	0.2126
D1	4.11	4.31	0.1618	0.1697
D2	4.80	5.00	0.1890	0.1969
E	5.95	6.15	0.2343	0.2421
E1	5.65	5.85	0.2224	0.2303
E2	1.60	/	0.0630	/
e	1.27 BSC		0.05 BSC	
L	0.05	0.25	0.0020	0.0098
L1	0.38	0.50	0.0150	0.0197
L2	0.38	0.50	0.0150	0.0197
H	3.30	3.50	0.1299	0.1378
I	/	0.18	/	0.0070