



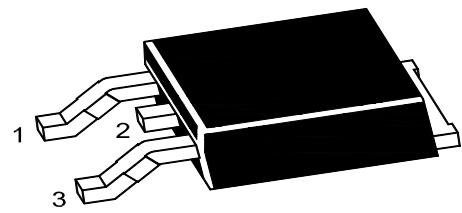
PJM80H02NTE

N-Channel Enhancement Mode Power MOSFET

Features

- Fast Switching
- Low Gate Charge and RDS(on)
- Low Reverse transfer capacitances
- V_{DS}= 800V, I_D= 2A
R_{DS(on)}< 5.5Ω @V_{GS}= 10V

TO-252

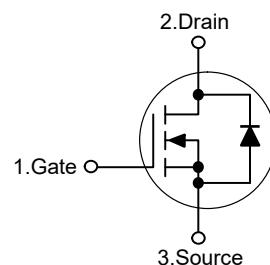


1. Gate 2.Drain 3.Source

Applications

- Power switch circuit of adaptor and charger

Schematic Diagram



Absolute Maximum Ratings

Ratings at 25°C ambient temperature unless otherwise specified.

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V _{DS}	800	V
Gate-Source Voltage	V _{GS}	±30	V
Drain Current-Continuous	I _D	2	A
Drain Current-Pulsed ^{Note1}	I _{DM}	8	A
Single pulse avalanche energy ^{Note4}	E _{AS}	70	mJ
Avalanche energy, Repetitive ^{Note1}	E _{AR}	5	mJ
Avalanche Current ^{Note1}	I _{AR}	1	A
Maximum Power Dissipation	P _D	40	W
Junction Temperature	T _J	150	°C
Storage Temperature Range	T _{STG}	-55 to +150	°C

Thermal Characteristics

Thermal Resistance,Junction-to-Ambient ^{Note2}	R _{θJA}	100	°C/W
Maximum Junction-to-Case ^{Note2}	R _{θJC}	3.12	°C/W



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Electrical Characteristics

(Ta=25°C unless otherwise specified)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =250μA	800	--	--	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =800V, V _{GS} =0V	--	--	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±30V, V _{DS} =0V	--	--	±100	nA
Gate Threshold Voltage ^{Note3}	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	2	3	4	V
Drain-Source On-Resistance ^{Note3}	R _{DS(on)}	V _{GS} =10V, I _D =1.5A	--	4.6	5.5	Ω
Forward Transconductance ^{Note3}	g _{FS}	V _{DS} =15V, I _D =1A	--	5.5	--	S
Dynamic Characteristics						
Input Capacitance	C _{iss}	V _{DS} =25V, V _{GS} =0V, f=1MHz	--	350	--	pF
Output Capacitance	C _{oss}		--	36	--	pF
Reverse Transfer Capacitance	C _{rss}		--	3.8	--	pF
Switching Characteristics						
Turn-on Delay Time	t _{d(on)}	V _{DD} =400V, I _D =3A, V _{GS} =10V, R _G =12Ω	--	7.5	--	nS
Turn-on Rise Time	t _r		--	5.5	--	nS
Turn-off Delay Time	t _{d(off)}		--	28	--	nS
Turn-off Fall Time	t _f		--	11	--	nS
Total Gate Charge	Q _g	V _{DD} =400V, I _D =3A, V _{GS} =10V	--	11	--	nC
Gate-Source Charge	Q _{gs}		--	1.8	--	nC
Gate-Drain Charge	Q _{gd}		--	5	--	nC
Source-Drain Diode Characteristics						
Diode Forward Voltage ^{Note3}	V _{SD}	V _{GS} =0V, I _S =2A	--	--	1.5	V
Diode Forward Current ^{Note2}	I _S		--	--	2	A

Note: 1. Repetitive Rating: Pulse width limited by maximum junction temperature.

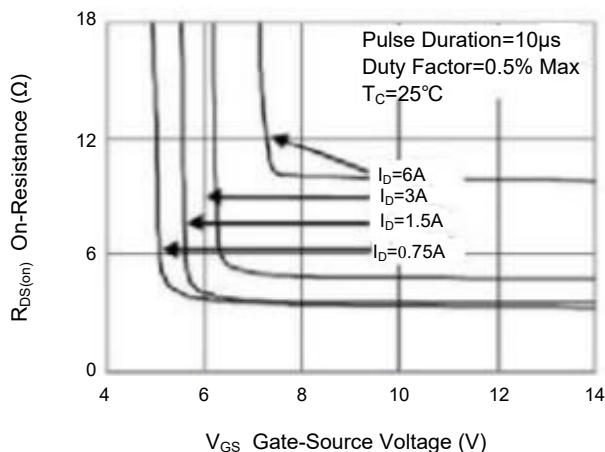
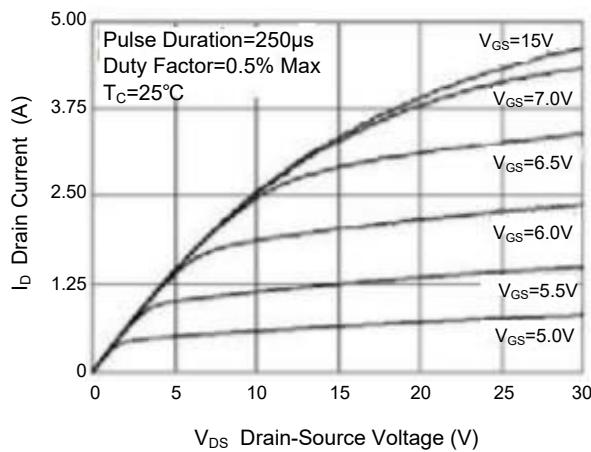
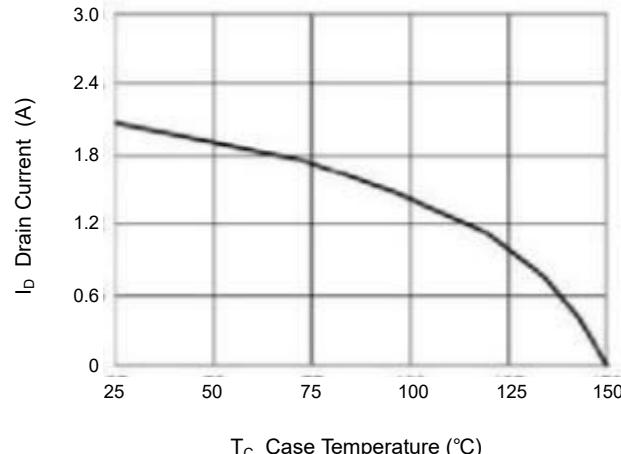
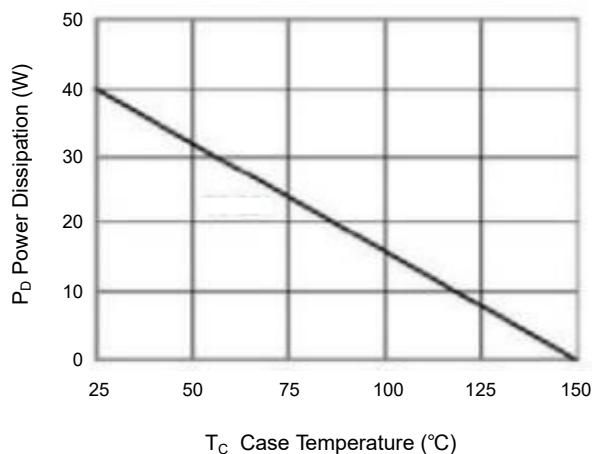
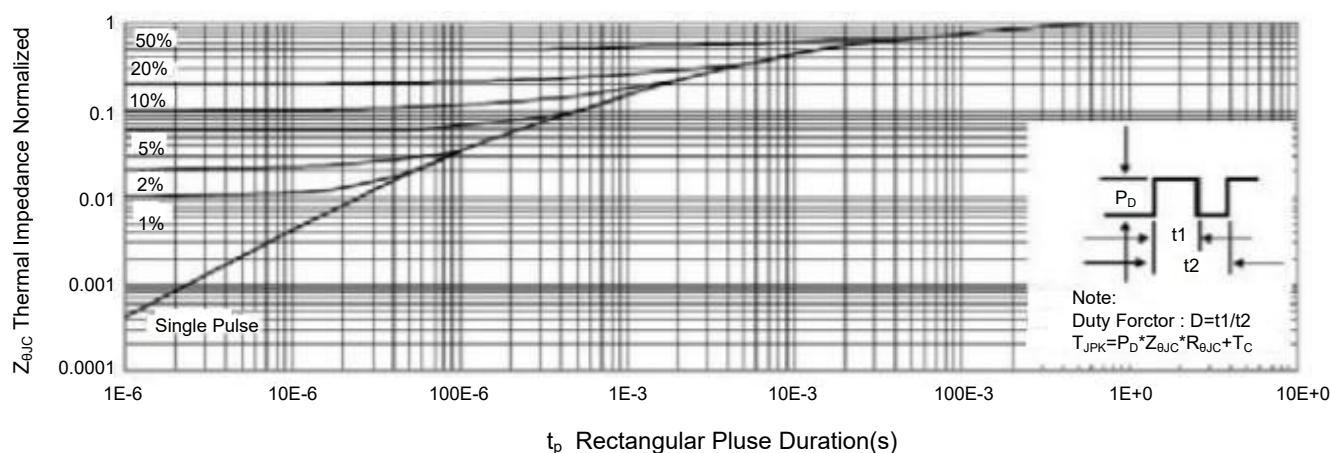
2. Surface Mounted on FR4 Board, t ≤ 10 sec.
3. Pulse Test: Pulse width < 380μs, duty cycle < 2%.
4. E_{AS} condition : L=10mH, I_D=4A, start T_J=25°C.



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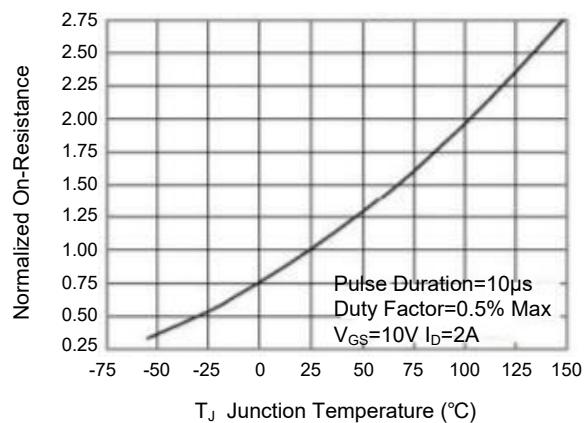
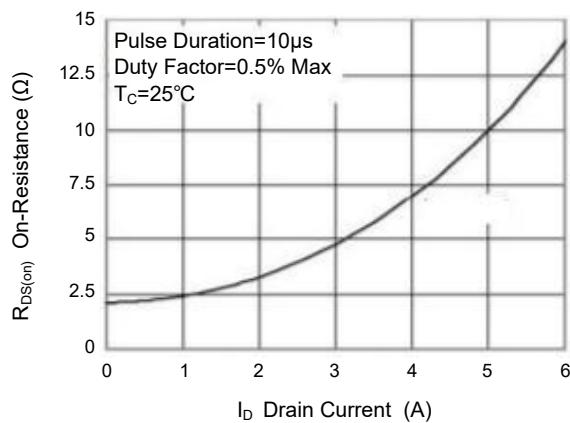
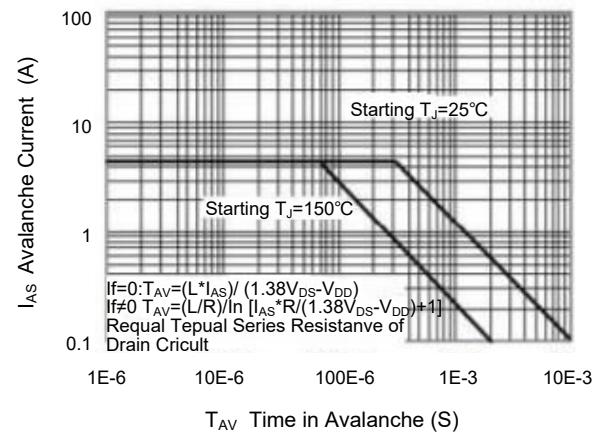
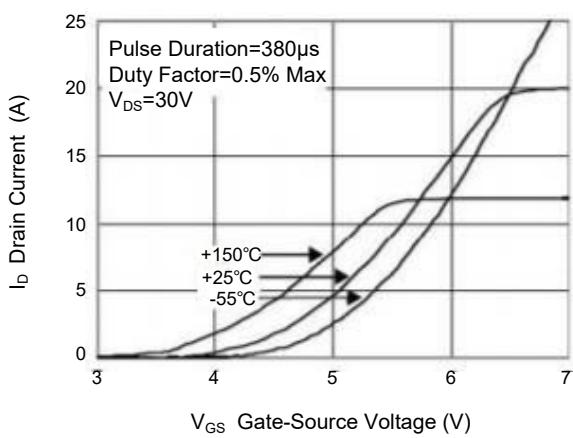
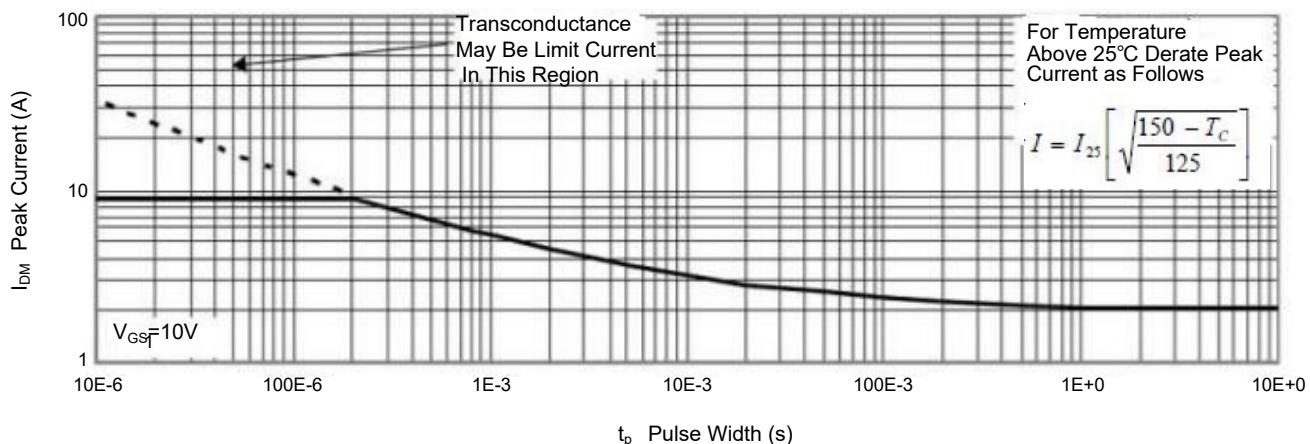
Typical Characteristic Curves





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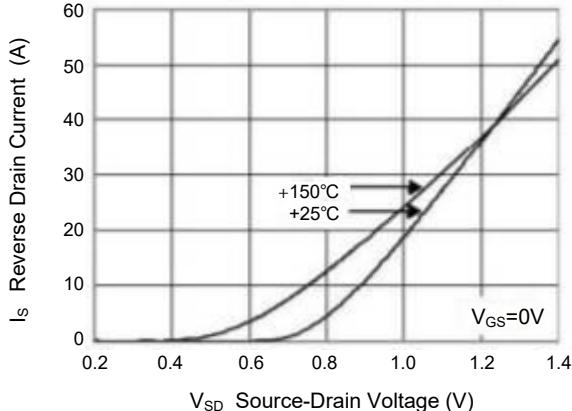
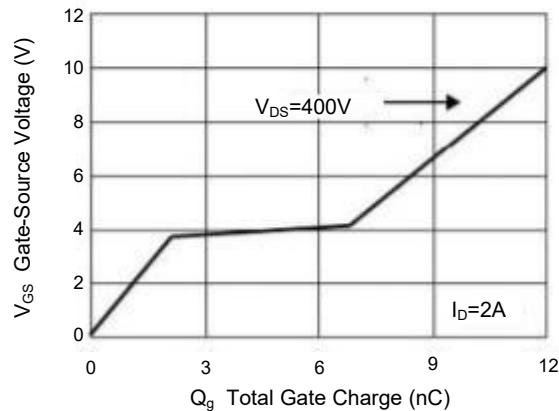
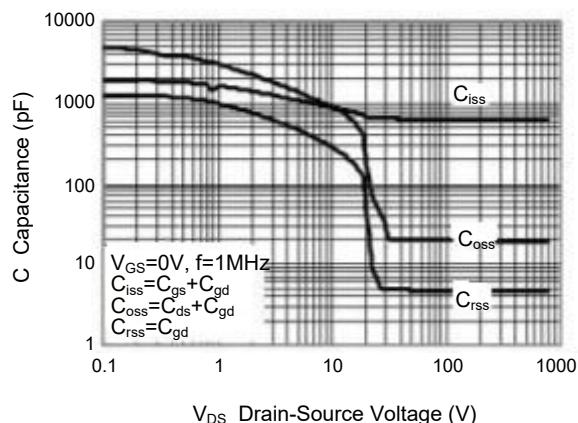
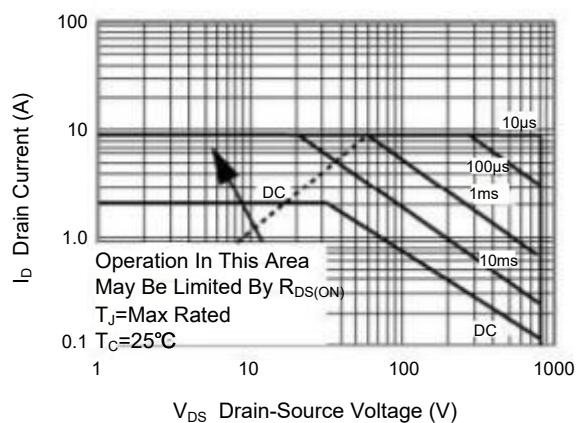
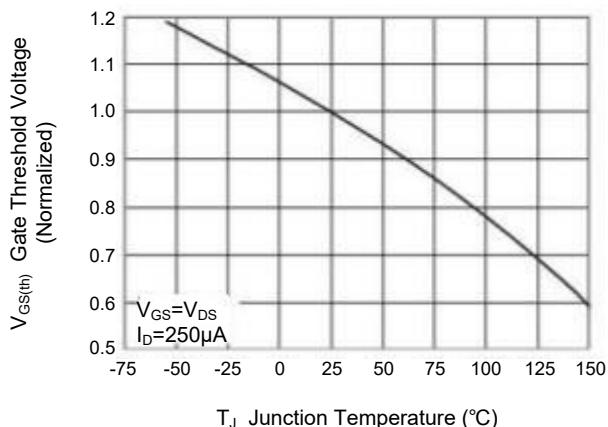
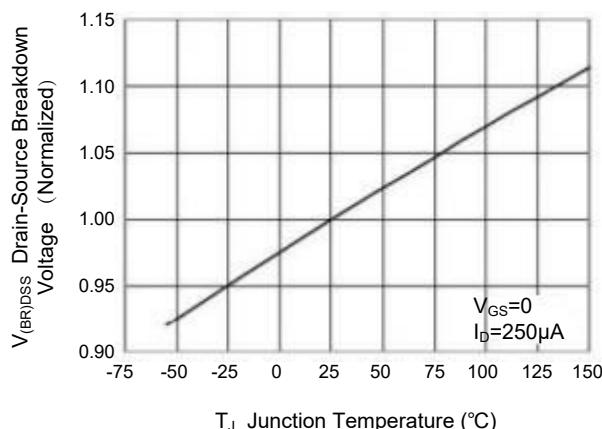
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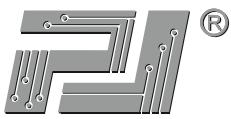




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Package Outline

TO-252

Dimensions in mm

