

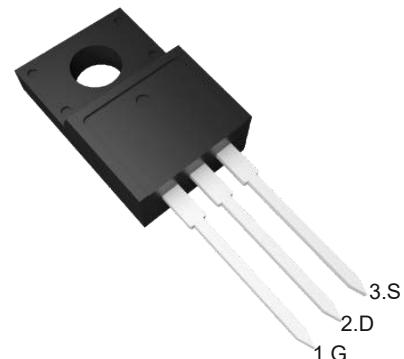
# PJM80H12NTF

## N-Channel Enhancement Mode Power MOSFET

### Features

- Fast Switching
- Low Reverse transfer capacitances
- Low gate charge and low  $R_{DS(on)}$
- $V_{DS} = 800V, I_D = 12A$
- $R_{DS(on)} < 0.65\Omega @ V_{GS} = 10V$

TO-220F

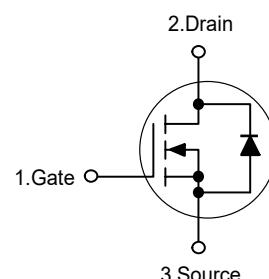


1.Gate 2.Drain 3.Source

### Applications

- Power switch circuit of adaptor and charger

Schematic diagram



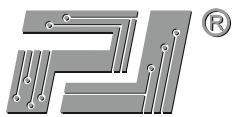
### Absolute Maximum Ratings

Ratings at 25°C case temperature unless otherwise specified.

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DS}$	800	V
Gate-Source Voltage	$V_{GS}$	$\pm 30$	V
Drain Current-Continuous	$I_D$	12	A
Drain Current-Pulsed <sup>Note1</sup>	$I_{DM}$	48	A
Single pulse avalanche energy <sup>Note4</sup>	$E_{AS}$	650	mJ
Maximum Power Dissipation	$P_D$	70	W
Junction Temperature	$T_J$	150	°C
Storage Temperature Range	$T_{STG}$	-55 to +150	°C

### Thermal Characteristics

Thermal Resistance, Junction-to-Ambient <sup>Note2</sup>	$R_{\theta JA}$	100	°C/W
Maximum Junction-to-Case <sup>Note2</sup>	$R_{\theta JC}$	1.79	°C/W



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## Electrical Characteristics

(T<sub>C</sub>=25°C unless otherwise specified)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	800	--	--	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =800V, V <sub>GS</sub> =0V	--	--	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±30V, V <sub>DS</sub> =0V	--	--	±100	nA
Gate Threshold Voltage <sup>Note3</sup>	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	2.0	--	4.0	V
Drain-Source On-Resistance <sup>Note3</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =4A	--	0.55	0.65	Ω
Forward Transconductance <sup>Note3</sup>	g <sub>Fs</sub>	V <sub>DS</sub> =20V, I <sub>D</sub> =12A	--	20	--	S
<b>Dynamic Characteristics</b>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, f=1MHz	--	3538	--	pF
Output Capacitance	C <sub>oss</sub>		--	240	--	pF
Reverse Transfer Capacitance	C <sub>rss</sub>		--	30	--	pF
<b>Switching Characteristics</b>						
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =400V, I <sub>D</sub> =12A V <sub>GS</sub> =10V, R <sub>G</sub> =4.7Ω	--	23	--	nS
Turn-on Rise Time	t <sub>r</sub>		--	12	--	nS
Turn-off Delay Time	t <sub>d(off)</sub>		--	83	--	nS
Turn-off Fall Time	t <sub>f</sub>		--	28	--	nS
Total Gate Charge	Q <sub>g</sub>	V <sub>DD</sub> =640V, I <sub>D</sub> =12A, V <sub>GS</sub> =10V	--	73	--	nC
Gate-Source Charge	Q <sub>gs</sub>		--	16	--	nC
Gate-Drain Charge	Q <sub>gd</sub>		--	27	--	nC
<b>Source-Drain Diode Characteristics</b>						
Diode Forward Voltage <sup>Note3</sup>	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>s</sub> =12A	--	--	1.5	V
Diode Forward Current <sup>Note2</sup>	I <sub>s</sub>		--	--	12	A

Note: 1. Repetitive Rating: Pulse width limited by maximum junction temperature.

2. Surface Mounted on FR4 Board, t ≤ 10 sec.

3. Pulse Test: Pulse width&lt;380μs, duty cycle&lt;2%.

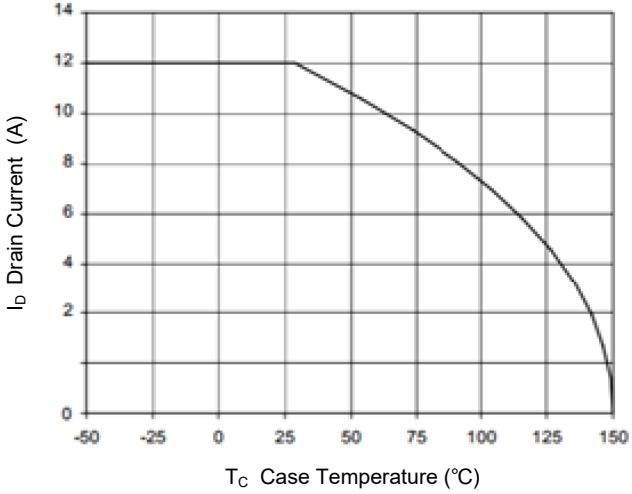
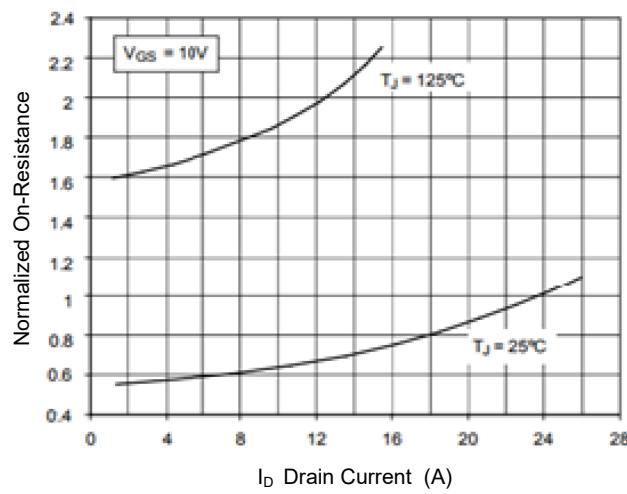
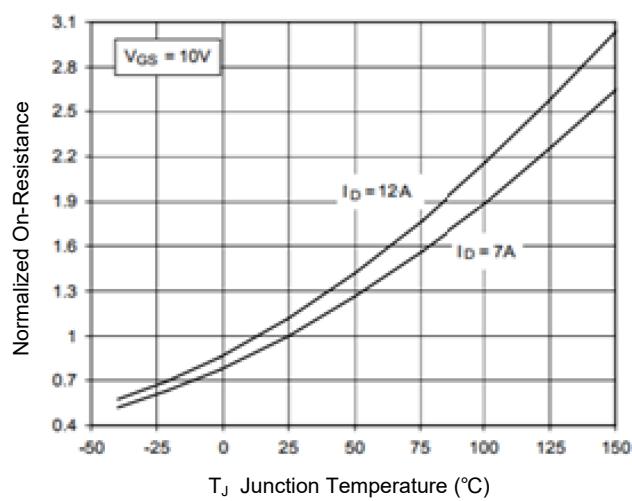
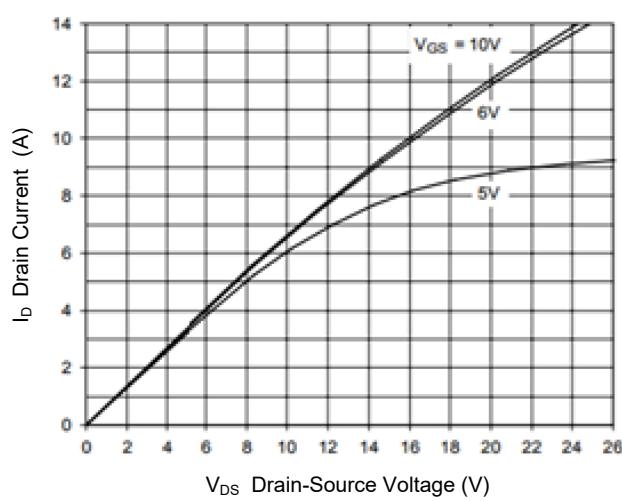
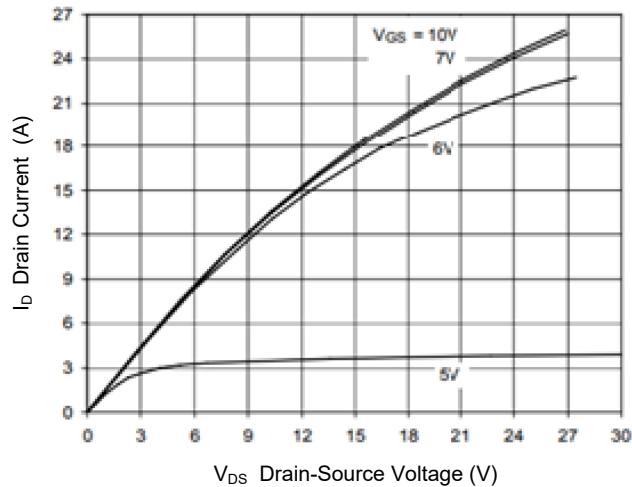
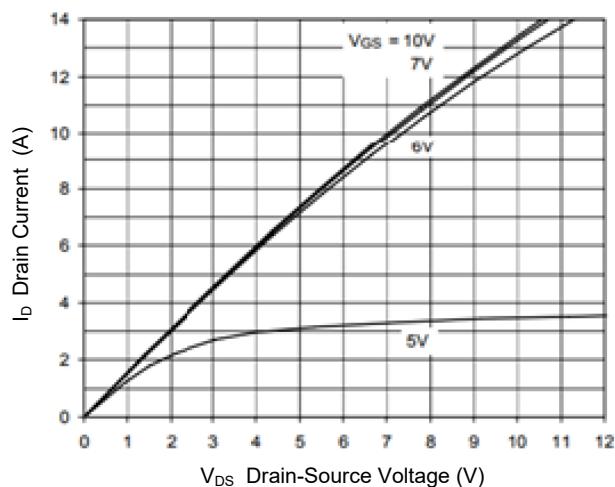
4. E<sub>AS</sub> Condition:L=10mH, I<sub>D</sub>=11.9A, start T<sub>J</sub>=25°C.



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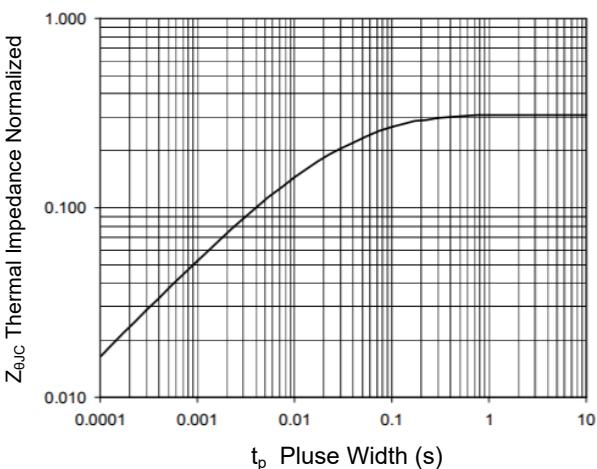
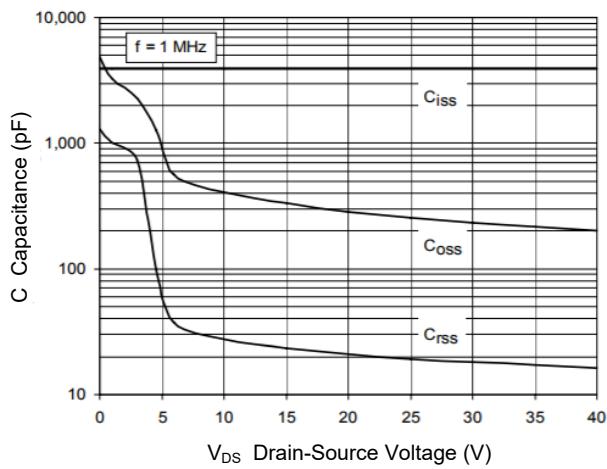
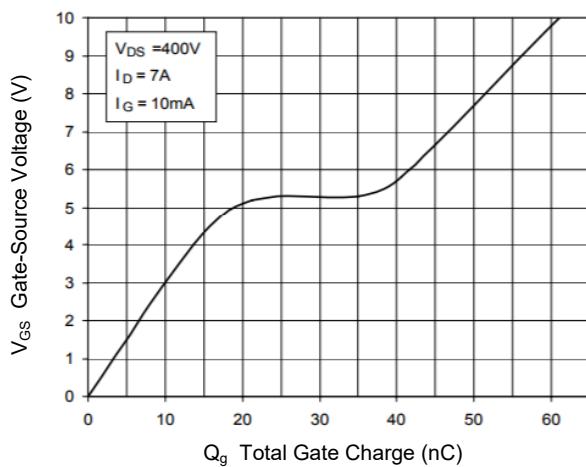
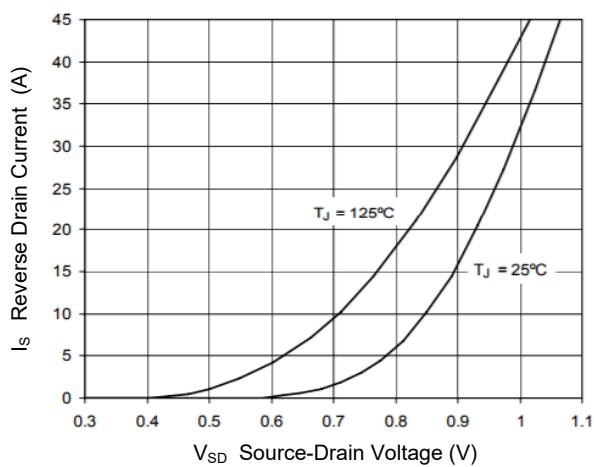
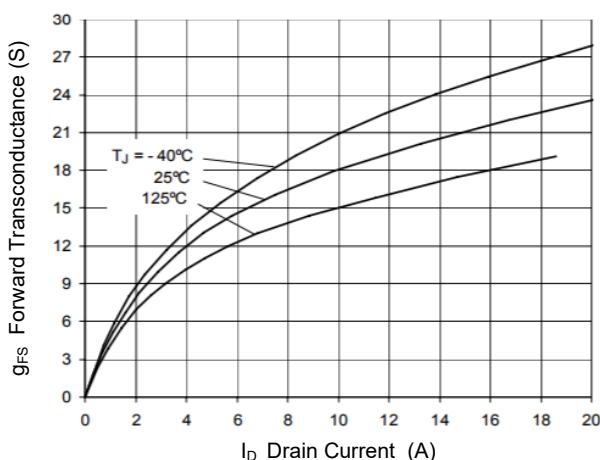
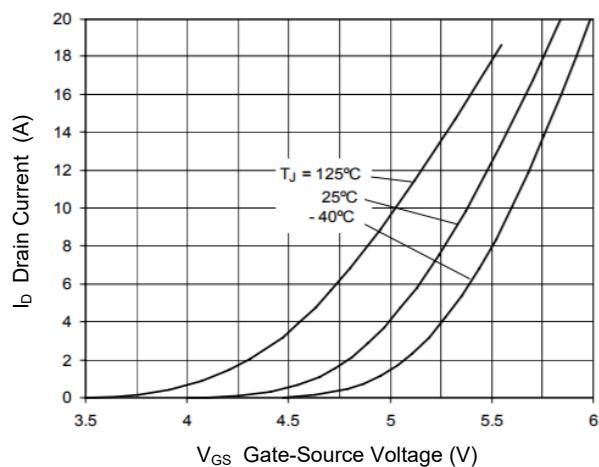
### Typical Characteristic Curves





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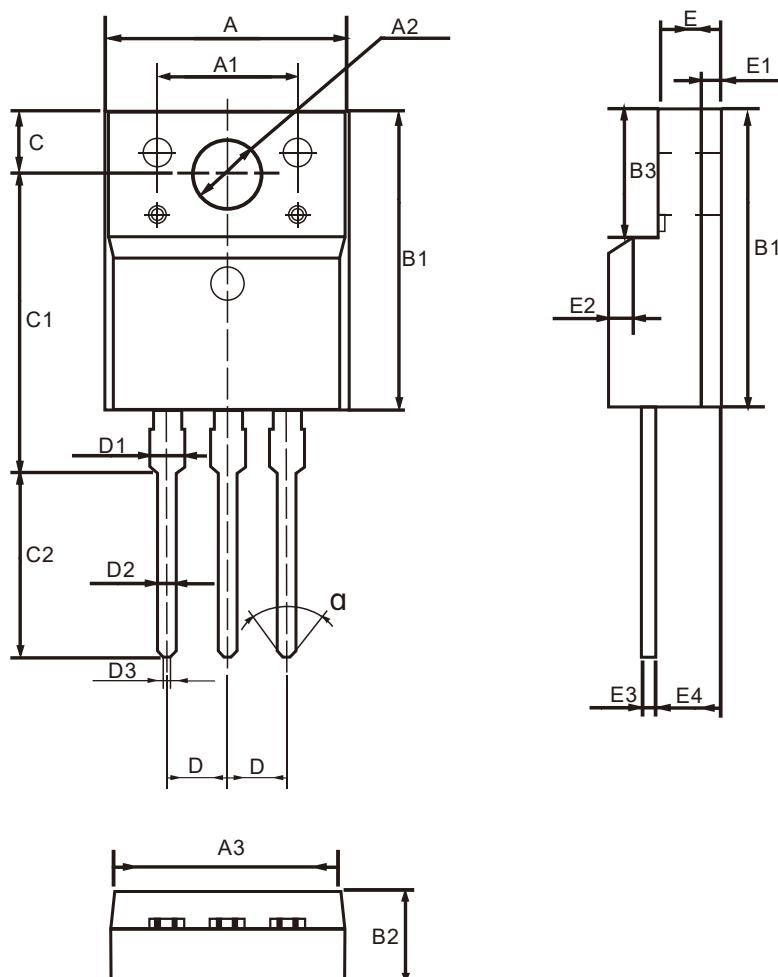




### Package Outline

TO-220F

Dimensions in mm



**TO-220F Package Dimensions**

UNIT : mm

SYMBOL	min	nom	max	SYMBOL	min	nom	max
A	9.80		10.60	D		2.54	
A1		7.00		D1	1.15		1.55
A2	2.90		3.40	D2	0.60		1.00
A3	9.10		9.90	D3	0.20		0.50
B1	15.40		16.40	E	2.24		2.84
B2	4.35		4.95	E1		0.70	
B3	6.00		7.40	E2		1.0 × 45°	
C	3.00		3.70	E3	0.35		0.65
C1	15.00		17.00	E4	2.30		3.30
C2	8.80		10.80	α (度)		30°	